

Appl. No. 09/821,485

Docket No. EMC2-093PUS

REMARKS

Applicants respectfully request the Examiner to reconsider and again examine the claims.

Claim 6 points out that:

each one of the front end portion of the director boards includes a plurality of director board gate arrays and a plurality of EDACs; and wherein each pair of the director board gate arrays is coupled between a corresponding pair of the adapter board gate arrays and a corresponding one of the EDACs. (emphasis added)

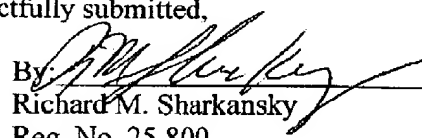
Thus, the claim points out that: (1) there is a plurality of EDACs; and (2) each pair of the director board gate arrays is coupled between a corresponding pair of the adapter board gate arrays and a corresponding one of the EDACs. Thus, each pair of gate arrays is coupled to a corresponding one of the plurality of EDACs. It is respectfully submitted that such an arrangement is not described or suggested in either Tuccio et al., U. S. Patent No. 6,289,401 or Sne et al., U. S. Patent No. 5,890,207. The Examiner's attention is directed to FIG. 9 of Sne et al, which shows that there is one EDAC for one gate array. Thus, Sne et al, does not describe a pair of gate arrays being coupled to a corresponding one of a plurality of EDACs.

The Examiner is respectfully invited to telephone the undersigning attorney if there are any questions regarding this Amendment or this application.

The Assistant Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-0889.

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Respectfully submitted,

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